

Fig. 1

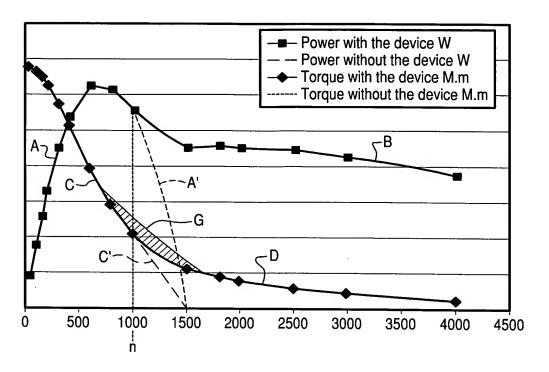


Fig. 2

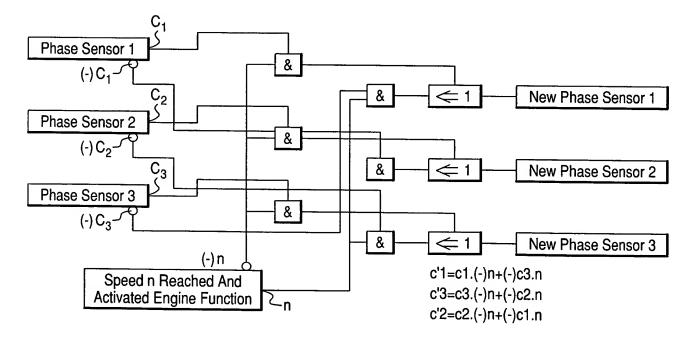


Fig. 3

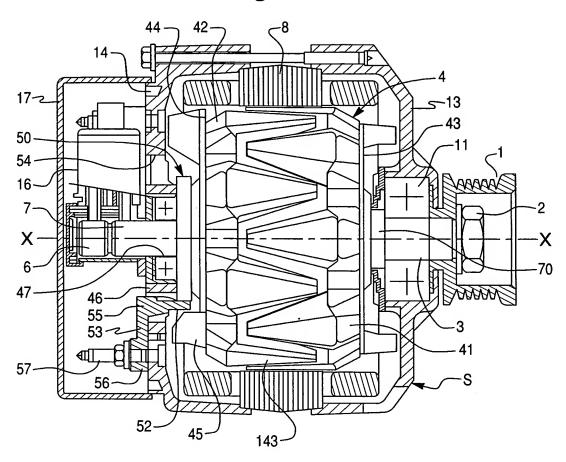


Fig. 4

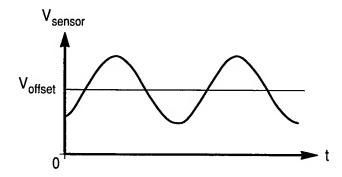


Fig. 5

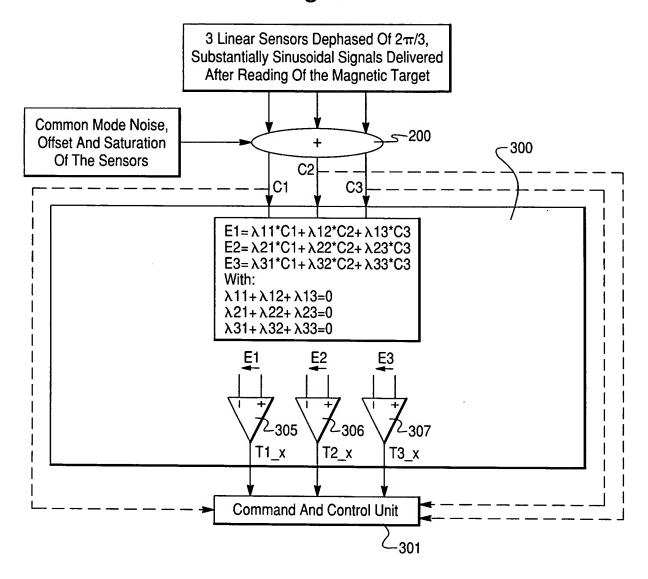


Fig. 6 C1 C2 C3 -304 305 T1 x **_302 ↑**E1 303--306 T2_x Digital-Analog Microcontroller **↑** E2 Converter -307 T3 x 303 **↑**E3

